SIJ-Provided Content Webinar 2022

Title: Designing PC Boards for Low EMI

Presenter: Ken Wyatt, Wyatt Technical Services, Inc.

Overview:
After helping clients get their products compliant for EMI, I've seen one underlying issue: poor PC board design. Poor routing and layer stack-up can cause endless delays and board spins due to radiated emissions failures or when on-board energy sources disrupt wireless receiver circuits for cellular, GPS and Wi-Fi. This presentation explains, in simple terms, the physics behind signal propagation in circuit boards. Understanding this basic concept will greatly reduce the risk of EMC issues.

Presenter Bio:
Ken Wyatt, principal consultant, Wyatt Technical Services, Inc., holds degrees in biology and electronic engineering and has worked as a product development engineer for 10 years for various aerospace firms on projects ranging from DC-DC power converters to RF and microwave systems for shipboard and space platforms. For over 20 years, he worked as a senior EMC engineer for Hewlett-Packard and Agilent Technologies in Colorado Springs where he provided comprehensive EMC design and troubleshooting services. During that time, he developed and provided advanced EMC training and corporate leadership for EMC. Ken is a senior member of the IEEE and a long time member of the EMC Society where he served as their official photographer for 10 years.

A prolific author and presenter, he has written or presented topics including RF amplifier design, RF network analysis software, EMC design and troubleshooting of products and use of harmonic comb generators for predicting shielding effectiveness. His specialty is EMI troubleshooting and is a co-author of the popular EMC Pocket Guide and RFI Pocket Guide. In 2014, he coauthored the book with Patrick André, EMI Troubleshooting Cookbook for Product Designers, with forward by Henry Ott.

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